

**ABSTRACT OF THE DISCLOSURE**

A graphics system includes a graphics processor for rendering graphics primitives with a shortened display list. A host processor generates a display list which includes a field load instruction for loading the display list into a register file. The graphics processor includes logic to encode and decode the field load instruction thereby shortening the display list loaded into the register file. The field load instruction may also be decoded to allow the graphics processor to randomly load the register file thereby shortening the processing of the display list.